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Application/Control Number: 10/618,439 Page 2

Art Unit: 2651

1. This Office action is in response to papers filed 10 July 2003, 3 November 2003, and 8 December 2003, which papers have been placed of record in the file.

2. Claims 1-25 are allowed over the prior art of record. The following is an examiner's statement of reasons for allowance:

Claims 1, 11, and 18 are allowable over the prior art of record since the cited references taken individually or in combination fails to particularly disclose a method of self writing track locations of a disk in a disk drive and a hard disk drive having servo burst correction comprising calculating repeatable runout correction values for the first servo bursts, self writing second bursts along the track such that the first and second bursts define the track centerline, and positioning the second bursts using said correction values to compensate for runout in the first bursts and reduce overall track runout, as presented in the environment of claims 1, 11, and 18. It is noted that the closest prior art, Szita et al., shows a method of reducing self writing track RRO error values for each servo burst similar to the claimed invention. However, Szita et al. fails to disclose positioning the second bursts using said correction values to compensate for runout in the first bursts as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Art Unit: 2651

3. The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure. Brunnett et al. Figures 4A-6, Ehrlich et al. Figures 11-12, and Szita Figures 5-6 are

RRO correction systems similar to applicant's invention.

4. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to James L Habermehl whose telephone number is (703)305-6975.

The examiner can normally be reached on 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, David Hudspeth can be reached on (703)308-4825. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Habermehl/jlh

6 Mar 05

DAVID HUDSPETH
SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2600